IN THE SPECIFICATION

Amendments to the Specification:

Please replace paragraph 35 beginning on page 12, line 8 with the following rewritten paragraph:

--In one embodiment, charge pump 530 (FIG. 5) includes Schmitt circuit 710 (FIG. 7A) to avoid and prevent false locks and harmonic locks. Figure 7B is a graph 700 illustrating a signal lock of control signal 525, in accordance with an embodiment of the present invention. Graph 700 shows control signal 525 voltage values along an x-axis and clock speed values along a y-axis. For example, if CPU clock signal 515 can equal any value from about ~~0MHz to about~~ 400MHz to about 12 GHz, then a multiple of CPU clock signal 515 has a voltage value between about 0.3V 760 and about 0.8V 780. It should be apparent that the voltage values and clock speeds are exemplary and that many other values are possible within any defined voltage value range, as long as charge pump 530 increases and decreases control signal 525 to an appropriate voltage level to enable a precise signal lock. For example, if control signal 525 is above approximately 0.8V, then Schmitt circuit 710 decreases the voltage. Thus, by manipulating control signal 525, charge pump 530 avoids and prevents false locks and harmonic locks.--